

FIG. 1

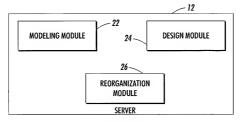


FIG. 2

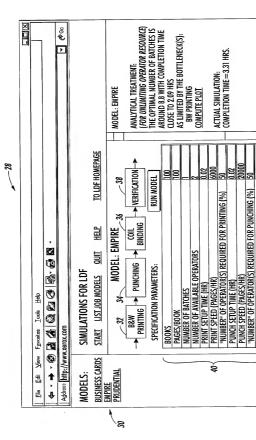


FIG. 3

COIL SETUP TIME (HR)

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Comp	Compute System Performance	_
Sleady State Productorn Efficiency	Average Buffer Level 0.0	

FIG.